



A1063

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Cheng H. Huang et al.
Application No. : 10/646,013
Confirmation No. : 8979
Filed : August 22, 2003
For : ELECTRICALLY-PROGRAMMABLE INTEGRATED
CIRCUIT ANTIFUSES
Group Art Unit : 2826
Examiner : Remmon R. Fordé

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97,
applicants wish to call the attention of the Examiner to the
document listed on the attached Form PTO-1449 (submitted in
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herewith.

A check in the amount of \$180.00 in payment of the fee
set forth in 37 C.F.R. §1.17(p) is enclosed herewith.

12/23/2004 09:05:03 00000004 1054503 100.00 02
06 1054503

Consideration of the foregoing in relation to this
patent application is respectfully requested.

Respectfully submitted,

G. Victor Treyz 12/14/04

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Reg. No. 36,294

Attorney for Applicants

Customer No. 45851

PTQ/SB/08b (08-03)

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Sheet	1	of	
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Complete if Known

Application Number	10/646,013
Filing Date	8/22/2003
First Named Inventor	Cheng H. Huang
Art Unit	2826
Examiner Name	Remmon R. Forde
Attorney Docket Number	A1063

NON PATENT LITERATURE DOCUMENTS

[illegible]

Examiner Signature		Date Considered	
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Sheet	1	of	1
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Examiner Name	Remmon R. Forde
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Attorney Docket Number	A1063
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NON PATENT LITERATURE DOCUMENTS

Examiner
Initials*Cite
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Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

 τ^2

V. KLEE et al. "A 0.13 μm logic-based embedded DRAM technology with electrical fuses, Cu interconnect in SiLK, sub-7ns random access time and its extension to the 0.10 μm generation," IEEE (c) 2001 IEDM (International Electron Devices Meeting), Technical Digest, pp 18.5.1-18.5.4

**Examiner
Signature**

Date
Considered

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